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Filed: August 5, 2003
Title: METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS
Attorney: John W. Crosby
Sierra Patent Group, Ltd.

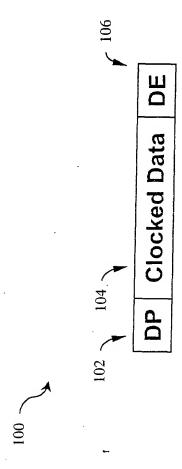


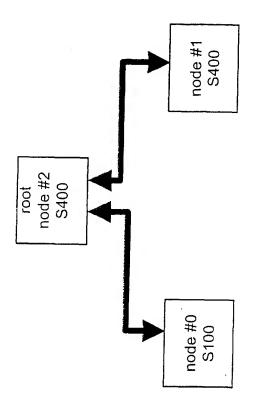
Fig.1 (Prior Art) Alpha Format Packet

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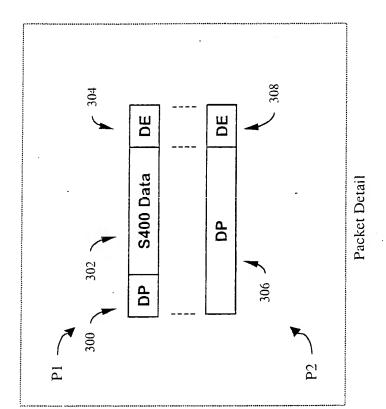


Legacy Compatibility Example Fig. 2 (Prior Art)

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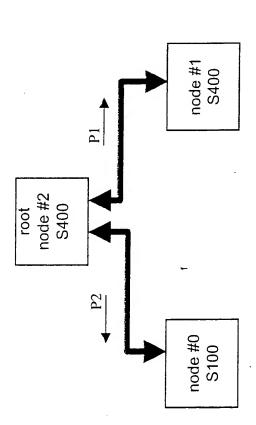


Fig. 3 (Prior Art) Legacy Speed Filtering Example

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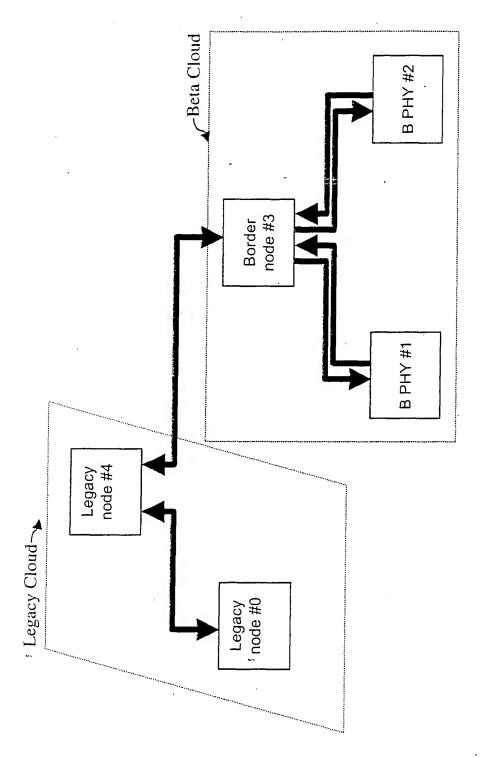
Fig. 4 (Prior Art) P1394b Packet Format

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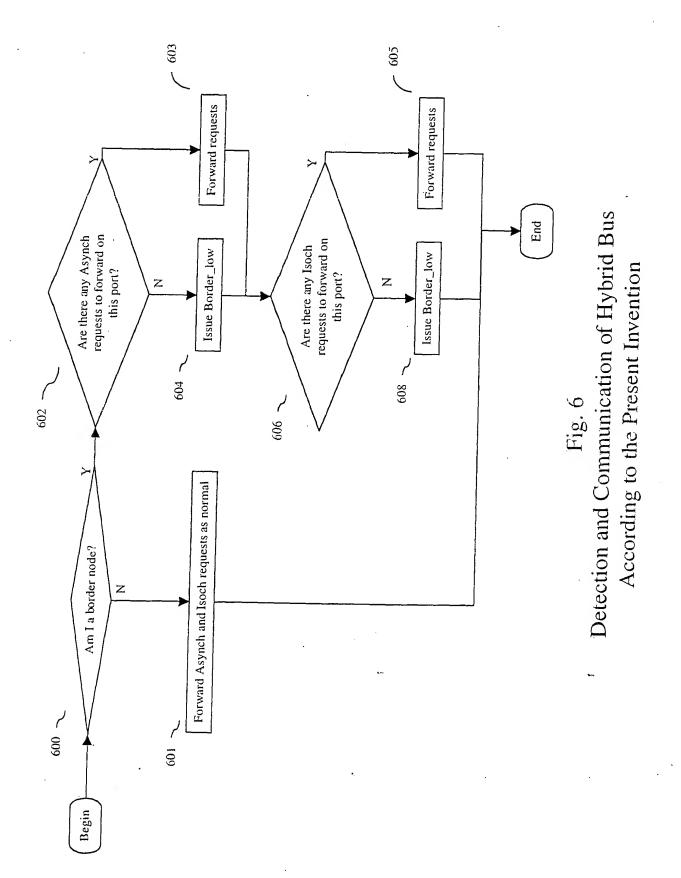


Data Communications System Having both P1394b and Legacy Devices Fig. 5 (Prior Art)

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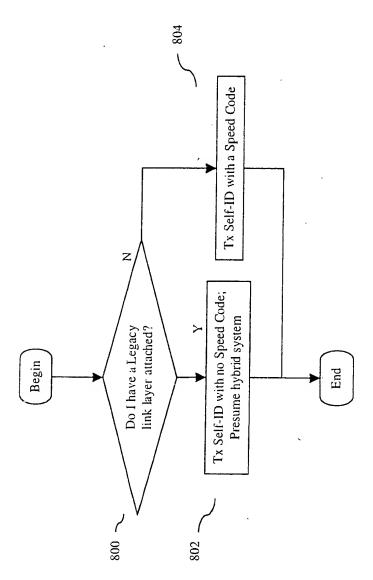
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\{ \overline{\text{DP1}} \overline{\text{Speed Code}} \overline{\text{Code}} \overline{\text{DP2}} \overline{\text{Data}} \overline{\text{Data}} \overline{\text{...}}

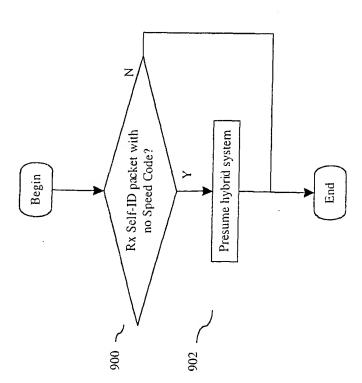
Fig. 7 (Prior Art) Legacy Format Packet Express Mail: EV263396209US Inventor: Hauck et al. Docket No.: APPL-P2834 DVD Page 8 of 13 Filed: August 5, 2003
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Detection and Communication of a Legacy Link Layer According to the Present Invention Fig. 8

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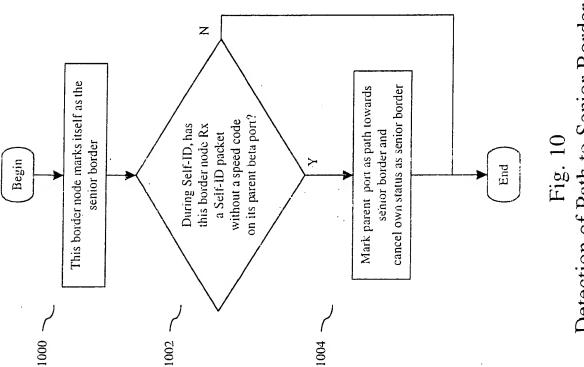


Detection and Communication of Hybrid Bus According to the Present Invention

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According to the Present Invention Detection of Path to Senior Border By a Border PHY

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Title: METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS
Attorney: John W. Crosby
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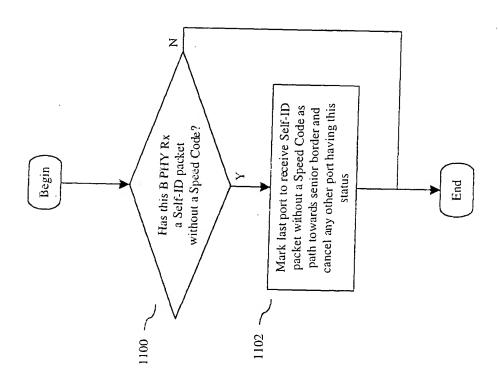
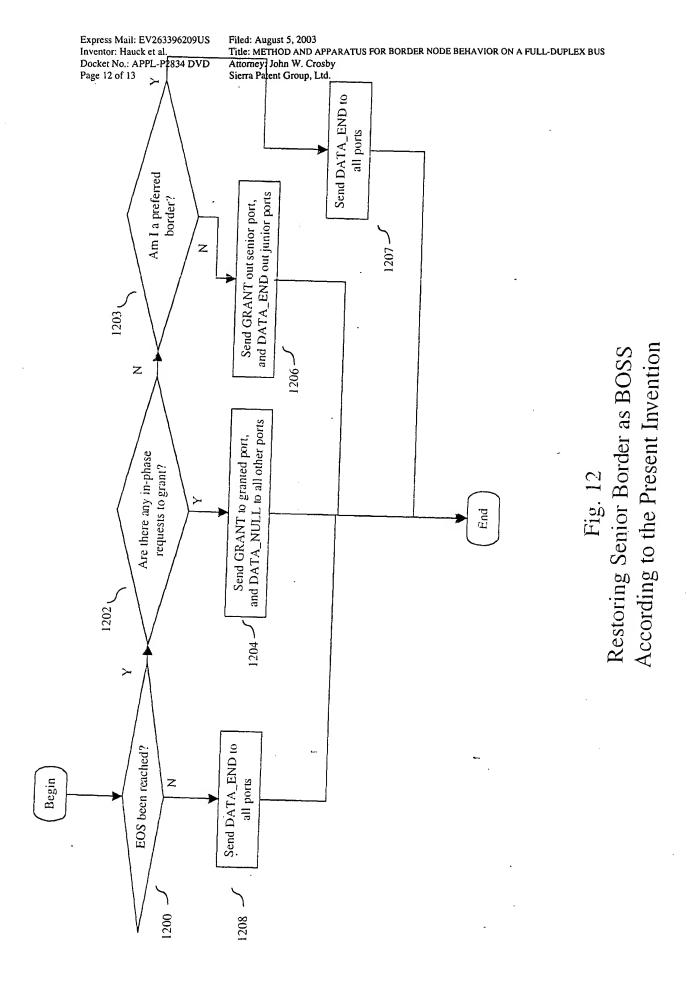


Fig. 11
Identification of Senior Border Node
By a B PHY
According to the Present Invention



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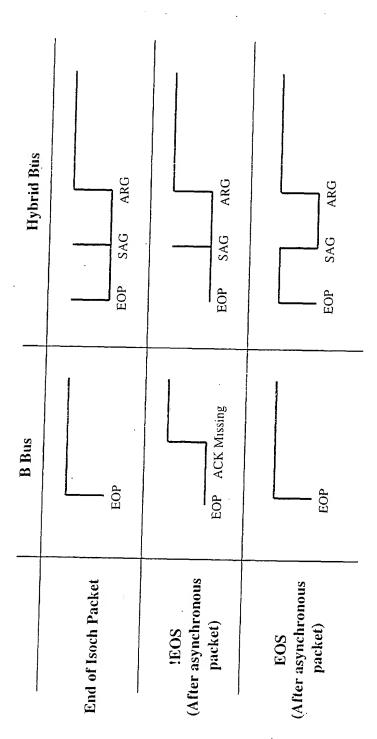


Fig. 13
OK_TO_GRANT Profile
According to the Present Invention